Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1262	714/738.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/05 10:53
L2	1236	714/726.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/05 10:54
L3	2863	714/718.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/05 10:54
L4	3217	714/724.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/05 10:54
L5	829	714/733.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/05 10:54
L6	3270	365/201.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/05 10:54
L7	521	365/45.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/05 10:55
L8	4459	324/765.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/05 10:55
L9	393	368/113.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/05 10:55
S1	7	(programmable adj pattern adj generator) with (IC or (integrated adj circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/05 10:53

S2	16	rout$3 with delay$3 with signal with select$5 with test$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 16:09
S3	5	rout$3 with delay$3 with signal with select$5 with test$3 with (circuit$1 or subcircuit$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 16:12
S4	35	(delay$3 adj signal) with select$5 with test$3 with (circuit$1 or subcircuit$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 16:17
S5	21	(delay$3 adj signal) with select$5 with test$3 with (circuit$1 or subcircuit$1) with input	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 16:21
S6	3	(delay$3 adj signal) with select$5 with (test$3 adj signal) with (circuit$1 or subcircuit$1) with input	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 16:24
S7	8	(delay$3 adj signal) with select$5 with (test$3 adj signal) with (circuit$1 or subcircuit$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 16:42
S8	0	((delay$3 adj signal) with select$5 with (test$3 adj signal) with (circuit$1 or subcircuit$1)) and (generat$3 with signal$1) and (monitor$3 with output with signal$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 17:08
S9	106	(selector$1 with (input adj terminal$1) with (IC or (integrated adj circuit)) with signal$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 17:12
S10	39	(selector$1 with (input adj terminal$1) with (IC or (integrated adj circuit)) with signal$1) same test$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 18:07
S11	53	(generat$3 with signal with (IC or (integrated adj circuit))) and (monitor$3 with output with signal) and ((test adj signal) with input with circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 18:10

S12	32	(generat$3 with signal with (IC or (integrated adj circuit))) and (monitor$3 with (output adj signal)) and ((test adj signal) with input with circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 18:16
S13	4	(generat$3 with signal with (IC or (integrated adj circuit))) and (monitor$3 with (output adj signal)) and ((test adj signal) with input with circuit) and BIST	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 18:20
S14	5	(generat$3 with signal with (IC or (integrated adj circuit))) and (monitor$3 with (output adj signal)) and ((test adj signal) with circuit) and BIST	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 18:21
S15	5	(generat$3 with signal with (IC or (integrated adj circuit))) and (monitor$3 with (output adj signal)) and ((test adj signal) with circuit$1) and BIST	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 18:21
S16	14	(generat$3 with signal with (IC or (integrated adj circuit))) and (monitor$3 with (output)) and ((test adj signal) with circuit$1) and BIST	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 18:28
S17	54	(generat$3 with signal with (IC or (integrated adj circuit))) and (monitor$3 with (output)) and BIST	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 18:28
S18	12	(generat$3 with signal with (IC or (integrated adj circuit))) and (monitor$3 with (output adj signal)) and BIST	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 18:40
S19	10	(generat$3 with signal with (IC or (integrated adj circuit))) and (monitor$3 with (output) with IC) and BIST	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/28 18:40
S20	2	"5948115".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/03 14:15
S21	61	multiplexer$1 same (test adj signal) same oscillat$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/03 14:16

S22	1	multiplexer$1 same (test adj signal) same oscillat$3 same IC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/03 14:17
S23	4	multiplexer$1 same (test adj signal) same oscillat$3 same (IC or (integrated adj circuit))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/03 14:17
S24	122	buffer$1 same (cascad$3 or seri$4) same capaci$4 same adjustable	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/04 10:03
S25	55	buffer$1 same (cascad$3 or seri$4) same (capaci$4 with adjustable)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/04 10:03
S26	29	(buffer$1 with (cascad$3 or seri$4)) same (capaci$4 with adjustable)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/04 10:03
S27	23	(buffer$1 with (cascad$3 or seri$4)) with (capaci$4 with adjustable)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/04 10:04
S28	23	buffer$1 with (cascad$3 or seri$4) with (capaci$4 with adjustable)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/04 10:04
S29	2	(buffer$1 with (cascad$3 or seri$4) with (capaci$4 with adjustable)) and (programmable adj delay)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/04 10:06
S30	13	(buffer$1 with (cascad$3 or seri$4) with (capaci$4 with adjustable)) and (delay with circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/04 10:07
S31	0	buffers with (cascad$3 or seri$4) with (capacitors with adjustable)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/04 10:08

S32	0	(buffers with (cascad$3 or seri$4)) same (capacitors with adjustable)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/04 10:11
S33	48	(buffers with (cascad$3 or seri$4)) and (capacitors with adjustable)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/04 10:08
S34	0	(buffers with (cascad$3 or seri$4)) and (capacitors with adjustable) and (delay adj circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/04 10:09
S35	1	(buffers with (cascad$3 or seri$4)) and (capacitors with adjustable) and (delay with circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/04 10:09
S36	2	(buffers with (cascad$3 or seri$4)) same (capacit$4 with adjustable)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/04 10:11
S37	32	(buffer$1 with (cascad$3 or seri$4)) same (capacit$4 with adjustable)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/04 10:12
S38	5	((buffer$1 with (cascad$3 or seri$4)) same (capacit$4 with adjustable)) and buffers	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/04 14:02
S39	743	oscillat$3 with function with delay	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/04 14:03
S40	283	signal with oscillat$3 with function with delay	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/04 14:41
S41	5	signal with oscillat$3 with function with delay with adjustable	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/04 14:36

| S42 | 14 | (signal with oscillat$3 with function with delay) and (measur$5 with period with signal) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/01/04 14:41 |